

REFERENCES

- [1] B. Sireesha and M. Sekhar, "VHDL MODELLING OF WI-FI MAC LAYER FOR TRANSMITTER," (IJCSIT) International Journal of Computer Science and Information Technologies, vol. Vol. 2 (5), 2011.
- [2] A. Troya, K. Maharatna, M. Krstic, E. Grass, and R. Kraemer, "Ofdm synchronizer implementation for an ieee 802.11 (a) compliant modem," 2002.
- [3] S. Banerji, "On IEEE 802.11: Wireless LAN Technology," arXiv preprint arXiv:1307.2661, 2013.
- [4] J. Brännström, "Implementation of an IEEE 802.11 a transmitter in VHDL for Altera Stratix II FPGA," 2006.
- [5] A. Bhavikatti and S. Kulkarni, "VHDL modeling of Wi-Fi MAC layer for transmitter," in Advance Computing Conference, 2009. IACC 2009. IEEE International, 2009, pp. 1-5.
- [6] J.-O. Jeong, "Hybrid FPGA and GPP Implementation of IEEE 802.15. 4 Physical Layer," Virginia Polytechnic Institute and State University, 2012.
- [7] E. Grayver, Implementing software defined radio: Springer, 2013.
- [8] J. W. Creswell, Research design: Qualitative, quantitative, and mixed methods approaches: Sage, 2013.
- [9] Y. Kim, H. Jung, H. H. Lee, and K. R. Cho, "MAC implementation for IEEE 802.11 wireless LAN," in ATM (ICATM 2001) and High Speed