

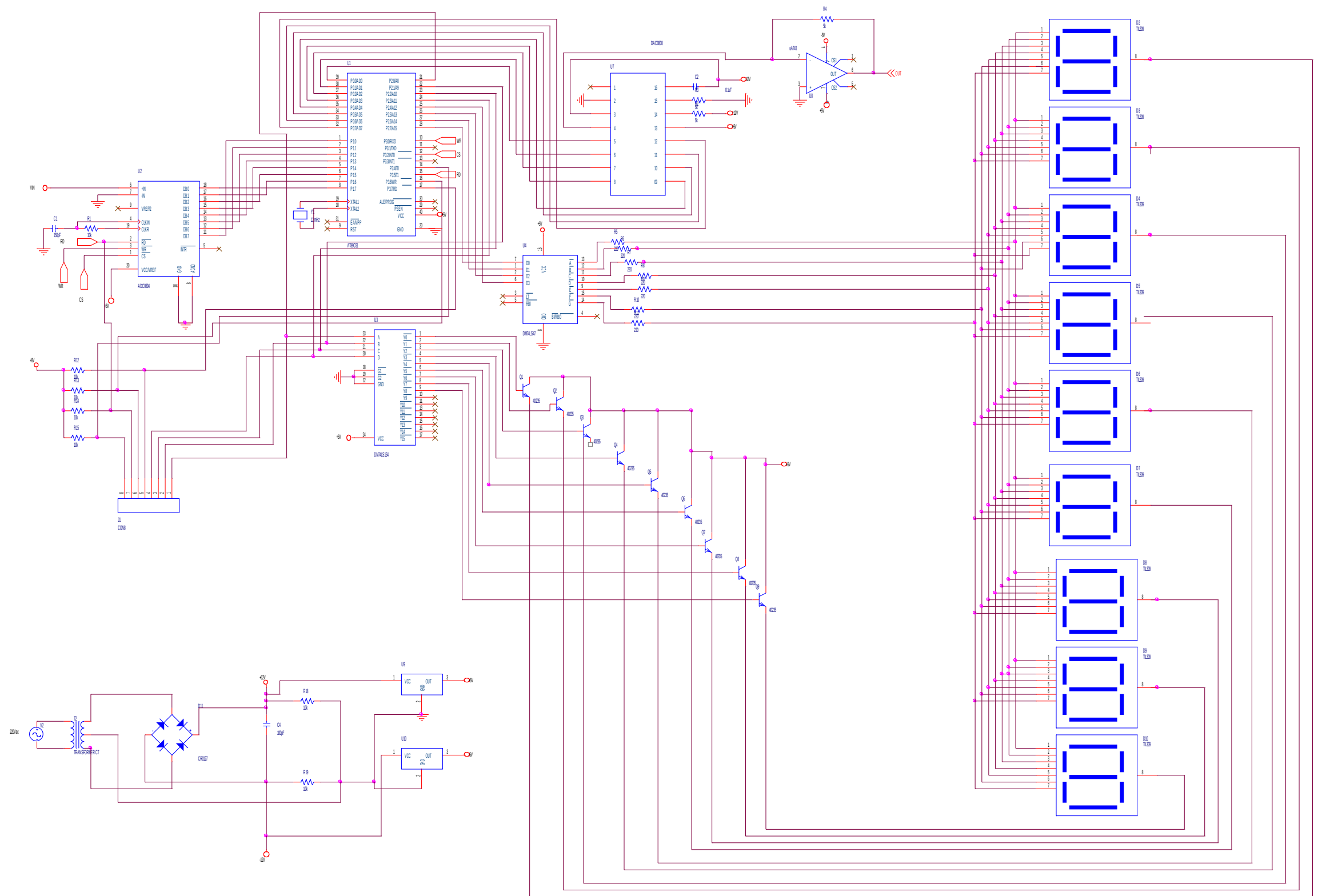


Fig. (5-3) Control Circuit