

Cellular Mobile			DTMF Encoder					Status Register (379)H				Data Register (378)H								Control Register(37a)H				HD74LS373 Latch									
F _{LOW}	F _{HIGH}	KEY	TOE	Q ₄	Q ₃	Q ₂	Q ₁	S7	S6	S5	S4	D7	D6	D5	D4	D3	D2	D1	D0	C3	C2	C1	C0	G	Q8	Q7	Q6	Q5	Q4	Q3	Q2	Q1	
697	1209	1	1	0	0	0	1	1	0	0	1	0	0	0	1	0	0	0	0	x	x	x	1	1	0	0	0	1	0	0	0	0	
697	1336	2	1	0	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	x	x	x	1	1	0	0	0	0	0	0	0	0	
697	1477	3	1	0	0	1	1	1	0	1	1	0	0	1	0	0	0	0	0	x	x	x	1	1	0	0	1	0	0	0	0	0	
770	1209	4	1	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	x	x	x	1	1	0	0	0	0	0	0	0	0	
770	1336	5	1	0	1	0	1	1	1	0	1	0	1	0	0	0	0	0	0	x	x	x	1	1	0	1	0	0	0	0	0	0	
770	1477	6	1	0	1	1	0	1	1	1	0	0	0	0	0	0	0	0	0	x	x	x	1	1	0	0	0	0	0	0	0	0	
852	1209	7	1	0	1	1	1	1	1	1	1	0	0	0	0	X1	X2	X3	X4	x	x	x	1	1	0	0	0	0	X1	X2	X3	X4	
852	1336	8	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	x	x	x	1	1	0	0	0	0	0	0	0	0	
852	1477	9	1	1	0	0	1	0	0	0	1	0	0	0	0	X4	X3	X2	X1	x	x	x	1	1	0	0	0	0	X4	X3	X2	X1	
841	1209	0	1	1	0	1	0	0	0	1	0	1	0	1	0	0	0	0	0	x	x	x	1	1	1	0	1	0	0	0	0	0	
841	1336	*	1	1	0	1	1	0	0	1	1	1	0	1	1	0	0	0	0	x	x	x	1	1	1	0	1	1	0	0	0	0	
841	1477	#	1	1	1	0	0	0	1	0	0	1	1	0	0	0	0	0	0	x	x	x	1	1	1	1	0	0	0	0	0	0	
697	1633	A	1	1	1	0	1	0	1	0	1	1	1	0	1	0	0	0	0	x	x	x	1	1	1	1	0	1	0	0	0	0	
770	1633	B	1	1	1	1	0	0	1	1	0	1	1	1	0	0	0	0	0	x	x	x	1	1	1	1	1	0	0	0	0	0	
852	1633	C	1	1	1	1	1	0	1	1	1	1	1	1	0	0	0	0	0	x	x	x	1	1	1	1	1	1	0	0	0	0	
941	1633	D	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	x	x	x	1	1	0	0	0	0	0	0	0	0	
-	-	ANY	0	z	z	z	z	z	z	z	z	z	z	z	z	0	0	0	0	x	x	x	1	1	z	z	z	z	0	0	0	0	

Cellular Mobile	DTMF Encoder	ULN2001A Darlington Pair
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F _{LOW}	F _{HIGH}	KEY	TOE	Q ₄	Q ₃	Q ₂	Q ₁	C7	C6	C5	C4	C3	C2	C1	
697	1209	1	1	0	0	0	1	0	0	1	0	0	0	0	
697	1336	2	1	0	0	1	0	0	0	0	0	0	0	0	
697	1477	3	1	0	0	1	1	0	1	0	0	0	0	0	
770	1209	4	1	0	1	0	0	0	0	0	0	0	0	0	
770	1336	5	1	0	1	0	1	1	0	0	0	0	0	0	
770	1477	6	1	0	1	1	0	0	0	0	0	0	0	0	
852	1209	7	1	0	1	1	1	0	0	0	X1	X2	X3	X4	
852	1336	8	1	1	0	0	0	0	0	0	0	0	0	0	
852	1477	9	1	1	0	0	1	0	0	0	X4	X3	X2	X1	
841	1209	0	1	1	0	1	0	0	1	0	0	0	0	0	
841	1336	*	1	1	0	1	1	0	1	1	0	0	0	0	
841	1477	#	1	1	1	0	0	1	0	0	0	0	0	0	
697	1633	A	1	1	1	0	1	1	0	1	0	0	0	0	
770	1633	B	1	1	1	1	0	1	1	0	0	0	0	0	
852	1633	C	1	1	1	1	1	1	1	1	0	0	0	0	
941	1633	D	1	0	0	0	0	0	0	0	0	0	0	0	
-	-	ANY	0	z	z	z	z	z	z	z	0	0	0	0	

Output Truth Table Of Different Stages Of The Controller Circuit