

Acronyms.

ADC	Analog To Digital Converter
AGP	Accelerate Graphic Port
ALU	Arithmetic Logic Unit
BCD	Binary Coded Decimal
CAN	Controller Area Network
CAS	Column Address Select
CJC	Cold Junction Compensation
CPU	Central Processing Unit
CRT	Cathode Ray Tube
DAC	Digital To Analog Converter
DDR	Double Data Rate
DIMM	Dual Inline Memory Module
DMA	Direct Memory Access
DRAM	Dynamic Random Access Memory
DTMF	Dual Tone Multiple Frequency
ECP	Extended Capabilities Port
EDO	Extended Data Output
EEPROM	Electrically Erasable Programmable Read Only Memory
EISA	Extended Industrial Standard Architecture
EPP	Enhanced Parallel Port
EPROM	Erasable Programmable Read Only Memory
FET	Field Effect Transistor
FG	Floating Gate
FPM	Fast Page Module
GPIB	General Purpose Instrumentation Bus
HCD	Host Controller Device
HIPPI	High Performance Parallel Interface
I/O	Input / Output
IEEE	Institute Of Electrical And Electronics Engineers
IrDA	Infra Red Data Association
IRQ	Interrupt Request
ISA	Industrial Standard Architecture
KIPS	Kilo Instructions Per second
LCD	Liquid Crystal Display
LDR	Light Dependent Resistor
LSB	Least Significant Bit
MIPS	Millions of Instructions Per Second
MRAM	Magnetic Random Access Memory
MSB	Most Significant Bit
PCI	Peripheral Component Interconnect
PCMCIA	Personal Computer Memory Card International Association
PIC	Priority Interrupt Controller
PID	Proportional Integral Differential
PPI	Programmable Peripheral Interface
PROM	Programmable Read Only Memory
PSRAM	Pseudostatic Random Access Memory
R/W	Read / Write
RAM	Random Access Memory
RAS	Row Address Select
RDRAM	Rambus Dynamic Random Access Memory