

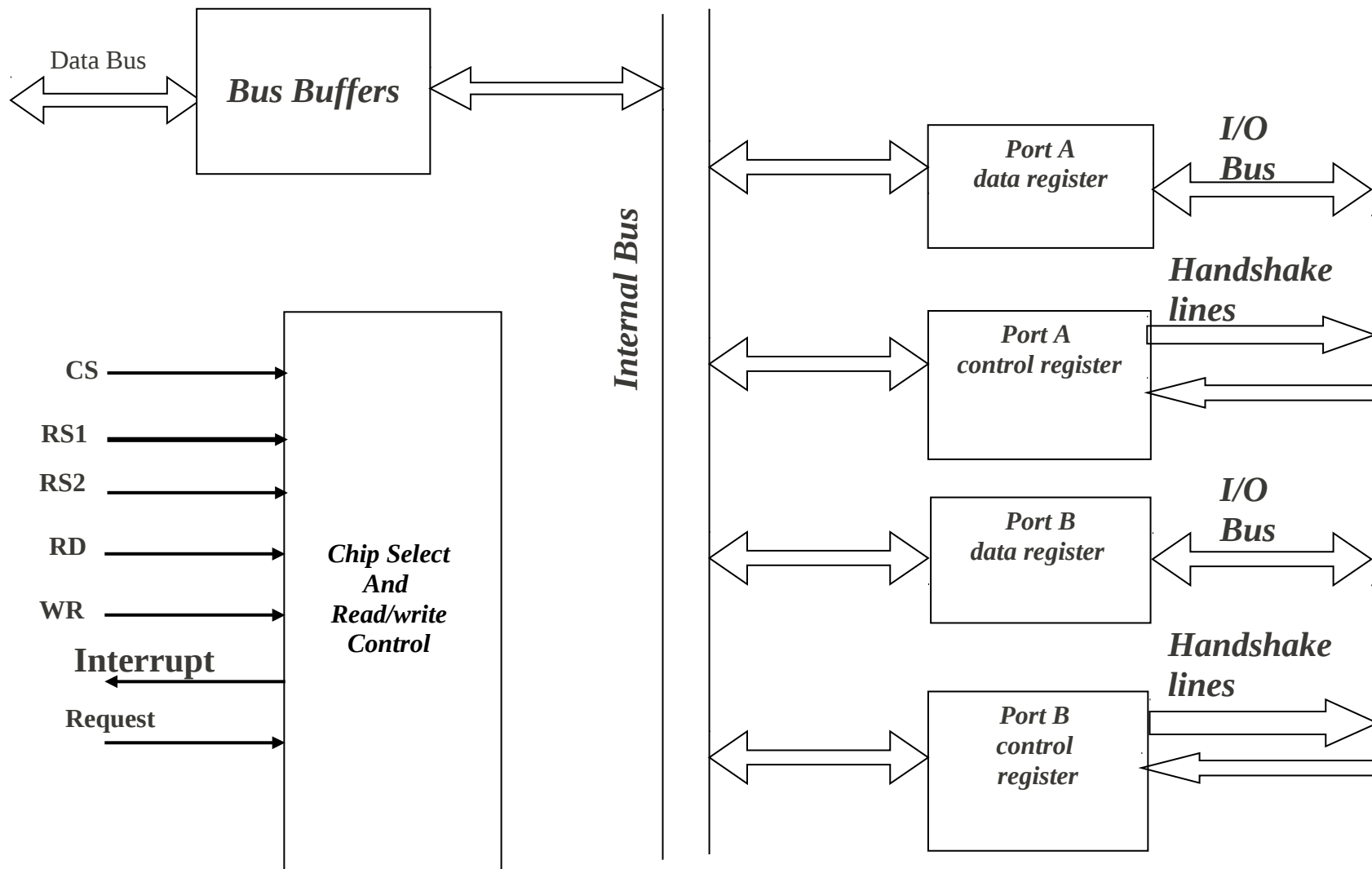


Figure (3.2) Block Diagram of parallel peripheral interface