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Width of reset pulse, tw(reset)

## TYPES SN54L90, SN54L93, SN74L90, SN74L93 DECADE AND BINARY COUNTERS

bsolute maximum ratings over operating free-air temperature rang	je (	un	les	ss c	oth	nei	rw	is	e r	101	te	d)				
Supply voltage, V <sub>CC</sub> (see Note 4)  Input voltage (see Note 5)  Operating free-air temperature range: SN54L90, SN54L93 Circuits  SN74L90, SN74L93 Circuits  Storage temperature range  IOTES: 4. Voltage values are with respect to network ground terminal.  5. Input voltages must be zero or positive with respect to network ground terminal.													-5	5°C 0°0	to 1	70°C
recommended operating conditions	Ts	N5	4L:	L90, SN54L93			S	N7	4L90, SN74L93			3	UNIT			
	1	MIN	J	NC	M	Λ	IΑ	X	N	/IIN	ı	NO	MC	MA	X	OIVII
7 A 12 A 1	$\top$	4.5	5		5		5.	5	4	.75	5		5	5.2	5	V
Supply voltage, VCC	+	(	)					3		(	0				3	MHz
Input count frequency, f <sub>count</sub>	+	-	_				10	0			_			-20	0	μА
High-level output current, IOH	+		-		_	_		2	$\vdash$	-	-				6	-
Low-level output current, IOL								_						J	.0	mΔ
										000	^					mA
Width of input count pulse, tw(count)	_	20	_						-	200	_				1	mA ns

Operating free-air temperature, TA electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

200

200

PARAMETER				TEST CONDITIONS <sup>†</sup>					UNIT		
			TEST CO				MAX	MIN	TYP‡	MAX	Oldi
					2			2			V
VIH	High-level input voltage						0.7			0.7	V
VIL	Low-level input voltage			V = 2 V	2.4	3.3		2.4	3,3		
V	High-level output voltage	SN541		$V_{IH} = 2 V$	2.4	3.2		2.4	3,2		V
VOH	riigii-level output vortage	SN741			2.4		0.3		0.15	0.3	
	I I I I I I I I I I I I I I I I I I I	SN54				0.15			0.13	0.4	V
VOL	Low-level output voltage	SN74	' VIL = 0.7 V,	IOL = MAX¶		0.2	0.4		0.2		-
	Input current at maximum input voltage	Any reset inpu	t	C, V <sub>I</sub> = 5.5 V			100			100	-
1		A input	Vcc = MAX				300			200	μΑ
11		B input					600			200	
		Any reset inpu	+				10			10	
	High-level input current		V <sub>CC</sub> = MAX,	V1 = 24 V			30			20	μA
11H		A input	- VCC - WAX,	V  2.4 v		-	60			20	
		B input			+		-0.18			0.18	
ΊL	Low-level input	Any reset inpu			-		-0.54	-		0.36	-
		A input	V <sub>CC</sub> = MAX,	$V_1 = 0.3 \text{ V}$				-		0.36	-
	current	B input		-		-1.08	-			-	
los	Short-circuit output curre	ent§	V <sub>CC</sub> = MAX		-3		-15	-		-15	-
Icc	Supply current		V <sub>CC</sub> = MAX,	See Note 3		4	7.2		3.2	6.6	m.

## switching characteristics, VCC = 5 V, TA = 25°C

				'L90			UNIT			
PARAMETER		TEST COM	MIN	TYP	MAX	MIN	TYP	MAX		
fmax	Maximum count frequency			3	6		3	6		MHz
tPLH	Propagation delay time, low-to-high-level $\mathbf{Q}_{D}$ output from input A	C <sub>L</sub> = 50 pF, See Figure 1	$R_L = 4 k\Omega$ ,		230	340		280	450	ns
<sup>t</sup> PHL	Propagation delay time, high-to-low-level QD output from input A				230	340		280	450	ns

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<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type. ‡All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C, SNot more than one output should be shorted at a time. ¶Outputs are tested at I<sub>OL</sub> = MAX plus the limit value for I<sub>IL</sub> for the B input. This permits driving the B input while maintaining full fan-out capability.

capability.

NOTE 3: I<sub>CC</sub> is measured with all outputs open, both R<sub>0</sub> inputs grounded following momentary connection to 4.5 V, and all other inputs