

54/74 FAMILIES OF COMPATIBLE TTL CIRCUITS

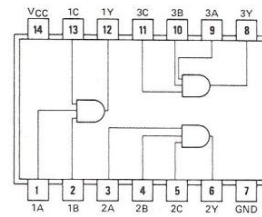
SSI GATES . . . LOGIC AND PIN ASSIGNMENTS (TOP VIEWS)

15

TRIPLE 3-INPUT
POSITIVE-AND GATES
WITH OPEN-COLLECTOR OUTPUTS

positive logic:
 $Y = ABC$

See page 96



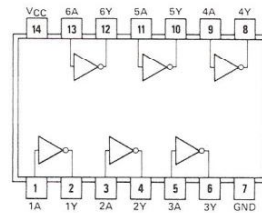
SN54H15/SN74H15(J, N, W)
SN54LS15/SN74LS15(J, N, W)
SN54S15/SN74S15(J, N, W)

16

HEX INVERTER BUFFERS/DRIVERS
WITH OPEN-COLLECTOR
HIGH-VOLTAGE OUTPUTS

positive logic:
 $Y = \overline{A}$

See page 106



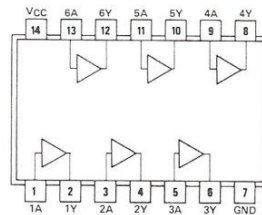
SN5416/SN7416(J, N, W)

17

HEX BUFFERS/DRIVERS
WITH OPEN-COLLECTOR
HIGH-VOLTAGE OUTPUTS

positive logic:
 $Y = A$

See page 106



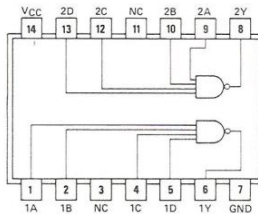
SN5417/SN7417(J, N, W)

20

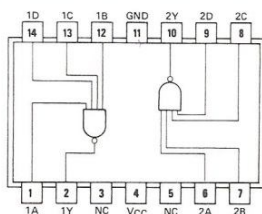
DUAL 4-INPUT
POSITIVE-NAND GATES

positive logic:
 $Y = \overline{ABCD}$

See page 86



SN5420/SN7420(J, N)
SN54H20/SN74H20(J, N)
SN54L20/SN74L20(J, N)
SN54LS20/SN74LS20(J, N, W)
SN54S20/SN74S20(J, N, W)



SN5420/SN7420(W)
SN54H20/SN74H20(W)
SN54L20/SN74L20(T)

NC—No internal connection