References

[1] Hossini Abdeldayam et. al,"Recent Advances in Photonic Devices For Optical Computing,"NASA Marshall Space Flight Center, Space Science Laboratory, 2004.

[2] http:\\ what is. Techtarget.com/definition/0,std9_gci 28399, 00.html. Accessed on 2.8.2004.

[3] W. E. Alexander, D.S Reeves, and C.S. Glosterjr, "Parallel Image Block Processing with the Data Parallel Architecture," Proceeding of The IEEE, Vol. 84, No. 7, Pp. 947_968, 1996.

[4] K. Park and T. Mizumto," Address Recognition and Generation Of Switching Control Signal For All Optical Routing," Eng., Vol. 38, Pp. 1848-1852, 1999.

[5] http: \\ www.win.tue.nt/~mhorst/bibliography/ bibliography/.html. Accessed on 5.10.2004.

[6] k. R. Chwdhury and S. Makhopadhyay, "An All-optical Wavelength Division Scheme for Parallel Addition Operation with Non-Linear Material," National Conference All Non-Linear System &Dynamics, Pp. 285-287, NCNSD, 2003.

[7]G.A. De Biase and A. Massini,"Redundant Binary Number Representation for an Inherently Parallel Arithmetic on Optical Number," APPL. Opt., Vol. 32, Pp. 669-664, 1993.

[8] http:\\ en.wikipedia.org/wiki/Von-Neuman-architecture. Accessed on 4.9.2004.

[9] http:\\ www.guusbaman.nl/downloads/thesis2003-0818.pdf. Accessed on 4.9.2004.

[10]http:\\encyclozine.com/Kosmoi/Computer/Arechitecture/Von-Neuman/. Accessed on 4.9.2004.

[11]S. Zhang and M.A. Karim, "Optical Arithmetic Processing Using Improved Redundant Binary Algorithm," Opt. Eng, Vol.32, No.3, Pp415-421, March 1999.

[12] P. C .Miller et. al, "Evaluation of an Optical Correllator Automatic Target Recognition System for Acquisition and Tracking in Densely Cluttered Natural Science," Opt. Eng., Vol. 38, Pp. 1814-1825, 1999.

[13] K. Janscek et. al, "Smart Optical Processor for Image Spectrometry," preparing for the future, Vol. 10, Pp. 6-8, 2000.

[14]T. J.Purcell et. al, "Ray Tracing on Programmable Graphics Hardware," Standard University.

[15] y. Incioka and J. Tanida." Optical Parallel Logic Gates Using A Shadow-Casting System for Optical Digital Computing", Proc. IEEE, Vol. 72, no. 7, July 1984.

[16] F. Pellacini et. al," Austere Interface for Interactive Cinematic Shadow Design," Program of Computer Graphic, Cornell University, Pp. 563-565, 2002.

[17] Peter Smid, "Symbolic Substitution: Overview," Department of Optics, Palacky University, Pp. 79-91, January 1999.

[18] N. I. An sari and M. Athrega," Pattern Recognition Using Optical Perception," National Institute of Eng., My sore, India, Pp. 568-572,

[19] http:\\ www.rmicamada.com/artide-er-e.pdf. Accessed on 2.4.2005.

[20] A. K. Cherri and M. S. Alam," Algorithms for Optoelectronic Implementation of Modified Signed–Digit Division, Square Root, Logarithmic and Exponential Function," Appl. Opt., vol. 40, Pp. 1236-1243, march 2001.

[21] H. Huang," Optical Scalable Parallel Modified Signed-Digit Algorithm for Logic Scale Array Addition and Multiplication Using Digit-Decomposition Plane Representation," Opt. Eng., Vol. 38, No. 3, Pp. 432-440, March 1999.

[22] A. K. Cherri," Signed-Digit Algorithm for Optical Computing And Pixel Assignment for Spatial Coding," Opt. Eng., Vol. 38, No. 3, Pp. 422-431, March 1999.

[23] http:\\ www.cs.umbc.edu/~phatak/611/projects/proj.html. Accessed on 3.4.2005.

[24] A. K. Cherri, M. S. Alam and A. A. S. Awwal," Optoelectronic Symbolic Substitution Based Canonical Modified Signed-Digit Arithmetic," Optic & Laser Tech., Vol. 29, Pp. 151-157, 1997

[25] A. K. Cherri," Classified One-Step High-Radix Signed-Digit Arithmetic Units," Opt. Eng., Vol. 37, Pp. 2324-2333, 1998.

[26].k. m. Iftekharuddin, A. A. S. Awwal and M. A. Salam," Signed- Digit Adder Using Electronically Addressable Spatial Light Modulator," Opt. Eng., Vol. 40, Pp. 2441-2445, Nov 2001

[27] http:\\ www.ece.ucsb.edu/faculty/parhami/ Accessed on 4.5.2005.

[28] W. Y. Salim, et. al, "One-Step Ternary Modified Signed-Digit Arithmetic Using a Efficient Encoding Scheme," Proc. SPIE, Paper 27, Vol. 4414, 30 July-4 Aug., 2000, VSA.

[29] M. S. Alam, A. K. Cherri and A. Chatterjea," Symmetrically Recorded Quaternary Signed-Digit Arithmetic Using a Shared Content Addressable Memory," Opt. Eng., Vol. 35, No. 4, Pp. 1141-1149, Apr. 1996.

[30] S. Zang and M. A. Karim," Programmable Modified Signed-Digit Addition Module Based on Binary Logic Gates," Opt. Eng., Vol. 33, No. 3, Pp. 456-461, March 1999.

[31] Light-Emitting Diodes (LEDs) and Laser Diodes: Implication for Hazard Assessment, Health Physics, Vol. 78, No. 6, Pp. 744-752, 2000- http:\\ www.incirp.dc/documents/led.pdf

[32] Donald G. Baker," Fiber Optic Design and Applications," Reston publishing company, Inc. Apprentice-Hall Company Reston, Virginia 22090, Pp. 46-59, 1985.