

**SUDAN UNIVERSITY OF SCIENCE AND TECHNOLOGY
COLLEGE OF POST GRADUATE STUDIES**

***REALIZATION OF FULL ADDER
USING REDUNDANT NUMBER
REPRESENTATION IN OPTICAL
COMPUTING***

**A THESIS SUBMITTED IN PARTIAL FULFILLMENT FOR THE
REQUIREMENT OF THE DEGREE OF M.SC. IN
MICROPROCESSOR AND ELECTRONIC CONTROL ENGINEERING**

SUBMITTED BY:

**AFAF AHMED MOHAMED MAHGOUB
B.Sc. (HONORS) IN ELECTRICAL CONTROL (2001)**

SUPERVISED BY:

Dr. ASHRAF GASIM ELSEID

AND

Dr. ALAA ALSAFFAR

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