54/74 FAMILIES OF COMPATIBLE TTL CIRCUITS

SSI GATES . . . LOGIC AND PIN ASSIGNMENTS (TOP VIEWS)

15
TRIPLE 3-INPUT
POSITIVE-AND GATES
WITH OPEN-COLLECTOR OUTPUTS

positive logic:
\[ Y = ABC \]

See page 96

16
HEX INVERTER BUFFERS/DRIVERS
WITH OPEN-COLLECTOR
HIGH-VOLTAGE OUTPUTS

positive logic:
\[ Y = \bar{A} \]

See page 106

17
HEX BUFFERS/DRIVERS
WITH OPEN-COLLECTOR
HIGH-VOLTAGE OUTPUTS

positive logic:
\[ Y = \bar{A} \]

See page 106

20
DUAL 4-INPUT
POSITIVE-NAND GATES

positive logic:
\[ Y = ABCD \]

See page 86

\[ \text{NC—No internal connection} \]